Experiment 14 **DIGITAL CLOCK INTERFACING USING 8051 USING KEIL AND PROTEUS**

**AIM:**

Write an assembly language program for Digital Clock Interfacing Using 8051 using Keil and Proteus

**SOFTWARE REQUIRED:**

* Keil software 5.
* Proteus 8 software.

**KEIL PROCEDURE:**1.open the software ,click on project and open new version project.

2.create a new project file

3. enter AT89C51

4.click no

5.click ctrl n and write code

6. open project and click target build

7. open target build and open source file and ADD, CLOSE

8. click target build

9. next debug start and stop

10.open peripherals and select port 2

11. run the program in debug

12. open project and click optional properties and in that give output as hexa file.

**PROTEUS PROCEDURE:**

**1. Open proteus by clicking run as administrator.**

**2. Open new project and enter the file name.**

**3. Click next, next, next and finish.**

**4. click P symbol and search keyword and place it’**

**AT89C51**

**7SEG-MPX4-CA**

**Using terminal click ground and place it two times.**

**5. Connecting pin number 21-1 up to 28-8**

**6. Connecting the ground to pin 20.**

**7. Connecting the pin no. 19-13 to the wire between pin 20 and GND.**

**8. Connecting pin 19 to the C1 and 18 to C2.**

**9. Connect C1 and C2**

**10. Connect another GND to the wire between C1 and C2.**

**11. Connect the crystal pin no.1 to C1 and pin 2 to C2.**

**12. Give input in crystal as 16MHz.**

**13. Give input to C1 and C2 as 33pF.**

**14. Give input to AT89C51 as HEX file.**

**PROGRAM:**

include <reg51.h>

#define msec 1

unsigned int arr[10]={0x40,0xF9,0x24,0x30,0x19,0x12,0x02,0xF8,0x00,0x10};

sbit d4=P1^0;

sbit d3=P1^1;

sbit d2=P1^2;

sbit d1=P1^3;

sbit d0=P1^4;

sbit d= P1^5;

unsigned int v1,v2,v3,v4,v0,v5,v6;

void delay(unsigned int count)

{

unsigned int j,k;

for (j=0;j<=count;j++)

for (k=0;k<=5;k++);

}

void main()

{

v1=v2=v3=v4=v0=v5=v6=0;

while(1)

{

{

v0=v0+1;

if(v0==130)

{

v0=0;

v1=v1+1;

}

P2=0xFF;

d = 1;

d3 = d2 = d4 = d0 = d1= 0;

P2 = arr[v1];

delay(msec);

if(v1==10)

{

v1=0;

v2=v2+1;

}

P2=0xFF;

d0 = 1;

d4 = d3 = d1 =d=d2= 0;

P2 = arr[v2];

delay(msec);

if(v2==6)

{

v2=0;

v3=v3+1;

}

P2=0xFF;

d1 = 1;

d2 = d4 = d3 =d=d0= 0;

P2 = arr[v3];

delay(msec);

if(v3==10)

{

v3=0;

v4=v4+1;

}

P2=0xFF;

d2 = 1;

d3 = d4 = d1 =d=d0= 0;

P2 = arr[v4];

delay(msec);

if(v4==6)

{

v4=0;

v5=v5+1;

}

P2=0xFF;

d3 = 1;

d0 = d2 = d1 =d=d4= 0;

P2 = arr[v5];

delay(msec);

if(v5==10)

{

v5=0;

v6=v6+1;

}

P2=0xFF;

d4 = 1;

d3 = d2 = d1 =d=d0= 0;

P2 = arr[v6];

delay(msec);

if(v6==1&&v5==2)

{

v1=0;

v2=0;

v3=0;

v4=0;

v5=0;

v6=0;

}

delay(msec);

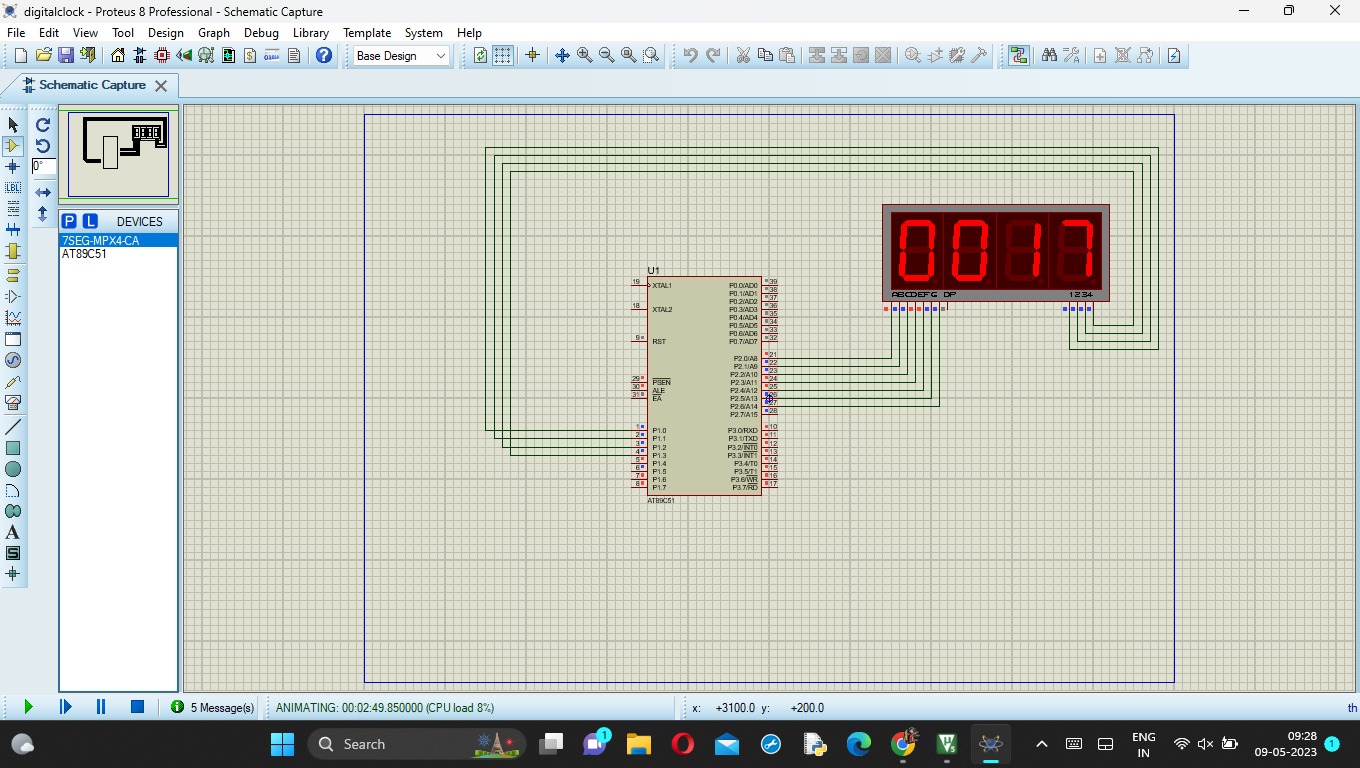
P2=0xFF;

}

}

}

Circuit Diagram:



**RESULT:**

Thus the program has been successfully verified and executed.